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	Application No.	Applicant(s)	
	10/707,068	IADANZA ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Naum B. Levin	2825 ·	
The MAILING DATE of this communication appearable claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGOT (The Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in this apport of the communication GHTS. This application is subject to	plication. If not included will be mailed in due course. THIS	
1. This communication is responsive to <u>Reponse 11/19/03</u> .			
2. ☑ The allowed claim(s) is/are <u>1-20</u> .			
3. ☐ Acknowledgment is made of a claim for foreign priority und a) ☐ All b) ☐ Some* c) ☐ None of the:			
1. Certified copies of the priority documents have			
2. Certified copies of the priority documents have			
 Copies of the certified copies of the priority doc International Bureau (PCT Rule 17.2(a)). 	uments have been received in this	national stage application from the	
* Certified copies not received:	%.		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONMETHIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a reply ENT of this application.	complying with the requirements	
4. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give:			
5. CORRECTED DRAWINGS (as "replacement sheets") must	t be submitted.		
(a) I including changes required by the Notice of Draftsperso	on's Patent Drawing Review (PTO-	948) attached	
1) ☐ hereto or 2) ☐ to Paper No./Mail Date			
(b) including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment or in the C	Office action of	
Identifying indicia such as the application number (see 37 CFR 1.8 each sheet. Replacement sheet(s) should be labeled as such in the			
 DEPOSIT OF and/or INFORMATION about the depos attached Examiner's comment regarding REQUIREMENT F 			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal P	atent Application (PTO-152)	
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary	(PTO-413),	
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08	Paper No./Mail Dat B), 7. 🔀 Examiner's Amendn	te nent/Comment	
Paper No./Mail Date			
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 	8! 🖾 Examiner's Stateme	ent of Reasons for Allowance	
	9. Other		
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DETAILED ACTION

1. This office action is in response to application 10/707,068. Claims 1-20 remain pending in the application.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Attorney Michael Lestrange (Reg. No. 53,207) on 10/04/2005.

The application has been amended as follows:

3. Claim 4:

replace lines 1 and 2 with -- The method of claim 1, further comprising providing a preferred components file containing a library of components selected from a --.

4. Claim 5:

line 4, replace "the group" with -- a group --.

5. Claim 10:

lines 6, replace "in synthesized" with – in the synthesized --.

6. Claim 14:

replace lines 1 and 2 with -- The method of claim 11, further comprising providing a preferred components file containing a library of components selected from a --.

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7. Claim 15:

line 4, replace "the group" with -- a group --.

8. Claim 20:

lines 7, replace "in synthesized" with - in the synthesized --.

Allowable Subject Matter

- 9. Claims 1-20 are allowed over prior art of record.
- 10. The following is an examiner's statement of reasons for allowance:

The prior art of record fails to teach or suggest or render obvious:

a method for designing an integrated circuit having multiple voltage domains, comprising generating a logical integrated circuit design from information contained in a high-level design file, said high-level design file defining global connection declarations and voltage domain connection declarations; synthesizing said logical integrated circuit design into a synthesized integrated circuit design based upon said information in a voltage domain definition file and a design constraint file; generating a noise model from said synthesized integrated circuit design based on information in said voltage domain definition file, a circuit level profile and a design constraint file; and simulating said noise model against constraints in said design constraint file and constraints in a circuit level profile file to determine if said synthesized integrated circuit design meets predetermined noise targets.

11. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should

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preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Naum B. Levin whose telephone number is 571-272-1898. The examiner can normally be reached on M-F (8:00-4:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NL

VUTHE SIEK PRIMARY EXAMINER